

# **Tatsuya Tsukamoto**

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## **Objective**

- To obtain a challenging position as a Process and Pixel Engineer where I can leverage my experience.

## **Summary**

- Experience in developing image sensors for consumer camera production.
- Semiconductor research experience at graduate school.

## **Professional History**

**SONY SEMICONDUCTOR MANUFACTURING INC., Kumamoto** Apr. 2023 - Present

- Device Development Engineer (Apr. 2023 - Present)  
Set up process flow of image sensor for consumer camera capable mass production.
  - Determined process conditions by communicating with process engineers and pixel design engineers.
  - Handled wafers to create prototypes.
  - Determined the optimal Cpk (value of process performance).

Achievement:

As a device section leader, completed process flow setup of prototype within target date (3 month).

## **Education**

**KYUSYU UNIVERSITY, Fukuoka** Apr.2021 - Mar. 2023

- Master of Engineering in Interdisciplinary Engineering Science  
Thesis Title: A simple sensor device for power cycle degradation sensing  
Power Device Engineering Lab, Prof. Wataru Saito

**ARIAKE NATIONAL COLLEGE OF TECHNOLOGY, Fukuoka** Apr. 2019 - Mar. 2021

- Bachelor of Engineering in Advanced Production and Information Systems Engineering  
Thesis Title: Development of rotary single raw hammering device for exterior wall tile testing robot  
Iwamoto Lab., Assoc. prof. Tatsuya Iwamoto

## **Additional Information**

- TOEIC(IP) 785, Nov. 2023
- Peer-reviewed journal articles; Tatsuta Tsukamoto, Shin-ichi Nishizawa, Wataru Saito, A simple sensor device for power cycle degradation sensing, Microelectronics Reliability, 10.1016/j.microrel.2023.115068, 147, 11, 115068, 2023.08.
- Received Student Encouragement Award at the Electronic Device/Semiconductor Power Conversion Joint Workshop, Institute of Electrical Engineers of Japan, 2022.12.